



Attorney Docket No. 1075.1257

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:

Masato SUSUKI, et al.

Application No.: 10/815,825

ATTENTION

Confirmation No.: 8878

CERTIFICATE OF CORRECTION

Filed: April 1, 2004

BRANCH

U.S. Patent No.: 7,038,955

Batch No.:

Issued: May 2, 2006

For: SEMICONDUCTOR DEVICE AND TESTING APPARATUS FOR SEMICONDUCTOR  
DEVICE

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Certificate  
JUN 27 2006  
of Correction

Sir:

Patentees respectfully request that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: June 22, 2006

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JUN 28 2006

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO : 7,038,955

DATED : May 2, 2006

INVENTOR(S) : Masato SUSUKI, et al.

Col. 14, line 14, delete "RAM(S)" and insert —RAM(s)—, therefor.

MAILING ADDRESS OF SENDER:

STAAS & HALSEY LLP  
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Washington, DC 20005

PATENT NO. 7,038,955

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